

**PART NUMBERING GUIDE**

**Environmental/Mechanical Specifications on page F5**

**OAH 100 48 A T - 30.000MHz**

<b>Package</b>		<b>Pin One Connection</b>
OAH = 14 Pin Dip / 5.0Vdc / HCMOS-TTL		Blank = No Connect, T = Tri State Enable High
OAH3 = 14 Pin Dip / 3.3Vdc / HCMOS-TTL		
OBH = 8 Pin Dip / 5.0Vdc / HCMOS-TTL		<b>Output Symmetry</b>
OBH3 = 8 Pin Dip / 3.3Vdc / HCMOS-TTL		Blank = 40/60%, A = 45/55%
<b>Inclusive Stability</b>		<b>Operating Temperature Range</b>
100= +/-100ppm, 50= +/-50ppm, 30= +/-30ppm, 25= +/-25ppm, 20= +/-20ppm, 15= +/-15ppm, 10= +/-10ppm		Blank = 0°C to 70°C, 27 = -20°C to 70°C, 48 = -40°C to 85°C

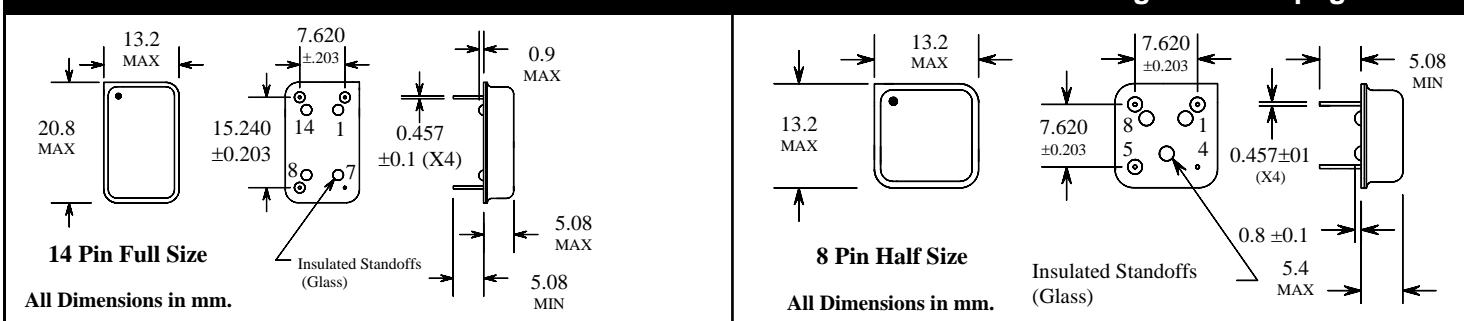
**ELECTRICAL SPECIFICATIONS**

**Revision: 1994-C**

<b>Frequency Range</b>		250kHz to 200.000MHz
<b>Operating Temperature Range</b>		0°C to 70°C / -20°C to 70°C / -40°C to 85°C
<b>Storage Temperature Range</b>		-55°C to 125°C
<b>Supply Voltage</b>		5.0Vdc ±10%, 3.3Vdc ±10%
<b>Input Current</b>	250.000kHz to 24.000MHz 24.001MHz to 50.000MHz 50.001MHz to 66.667MHz 66.668MHz to 200.000MHz	25mA Maximum 30mA Maximum 60mA Maximum 80mA Maximum
<b>Frequency Tolerance / Stability</b>	Inclusive of Operating Temperature Range, Supply Voltage and Load	±100ppm, ±50ppm, ±30ppm, ±25ppm, ±20ppm, ±15ppm or ±10ppm (20, 15, 10 = 0°C to 70°C Only)
<b>Output Voltage Logic High (Voh)</b>	w/TTL Load w/HCMOS Load	2.4Vdc Minimum Vdd -0.5Vdc Minimum
<b>Output Voltage Logic Low (Vol)</b>	w/TTL Load w/HCMOS Load	0.4Vdc Maximum 0.5Vdc Maximum
<b>Rise Time</b>	0.4Vdc to 2.4Vdc w/TTL Load; 20% to 80% of Waveform w/HCMOS Load < /=66.667MHz.	6nSeconds Maximum
<b>Fall Time</b>	0.4Vdc to 2.4Vdc w/TTL Load; 20% to 80% of Waveform w/HCMOS Load >66.667MHz.	3nSeconds Maximum
<b>Duty Cycle</b>	@1.4Vdc w/TTL Load; @50% w/HCMOS Load @1.4Vdc w/TTL Load or w/HCMOS Load @50% of Waveform w/LSTTL or HCMOS Load >66.667MHz	50 ±10% (Standard) 50±5% (Optional) 50±5% (Optional)
<b>Load Drive Capability</b>	250.000kHz to 24.000MHz 24.001MHz to 66.667MHz 66.668MHz to 150.000MHz	10TTL or 50pF HCMOS Load 10TTL or 15pF HCMOS Load 10LSTTL or 15pF HCMOS Load
<b>Pin 1 Tristate Input Voltage</b>	No Connection VIH VIL	Enables Output +2.2Vdc Minimum to Enable Output +0.8Vdc Maximum to Disable Output
<b>Aging (@ 25°C)</b>		±5ppm / year Maximum
<b>Start Up Time</b>		10mSeconds Maximum
<b>Absolute Clock Jitter</b>		±100pSeconds Maximum
<b>One Sigma Clock Jitter</b>		±25pSeconds Maximum

**MECHANICAL DIMENSIONS**

**Marking Guide on page F3-F4**



Pin 1: No Connect or Tri-State Pin 7: Case Ground	Pin 8: Output Pin 14: Supply Voltage	Pin 1: No Connect or Tri-State Pin 4: Case Ground	Pin 5: Output Pin 8: Supply Voltage
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